

Multilevel Solvers for Discontinuous Galerkin Methods

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Main Objective:

Design and analyze efficient solution techniques for *hp*-discontinuous Galerkin discretizations of

$$\begin{aligned} -\nabla \cdot (\mathbf{K} \nabla u) &= f && \text{in } \Omega \subset \mathbb{R}^d \\ u &= 0 && \text{on } \partial\Omega. \end{aligned}$$

Discontinuous Galerkin Formulation

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Weak formulation of PDE: find $u_h \in \mathcal{D}_k(\mathcal{E}_h)$ such that

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where the bilinear form a_ϵ is given by

$$\begin{aligned} a_\epsilon(u_h, v) = & \sum_{E \in \mathcal{E}_h} \int_E \mathbf{K} \nabla u_h \cdot \nabla v - \sum_{e \in \Gamma_h \cup \Gamma_D} \int_e \{\{\mathbf{K} \nabla u_h \cdot \vec{n}_e\}\} \llbracket v \rrbracket \\ & + \epsilon \sum_{e \in \Gamma_h \cup \Gamma_D} \int_e \{\{\mathbf{K} \nabla v \cdot \vec{n}_e\}\} \llbracket u_h \rrbracket + \sum_{e \in \Gamma_h \cup \Gamma_D} \frac{\sigma_e}{|e|^{\beta_0}} \int_e \llbracket u_h \rrbracket \llbracket v \rrbracket. \end{aligned}$$

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$$\mathbf{A}\vec{u} = \vec{f}.$$

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To solve this system we employ a Schwarz domain decomposition method:

$$B = \mathbf{R}_0 \mathbf{A}_0^{-1} \mathbf{R}_0^T + \sum_{j=1}^{N_S} \mathbf{R}_j \mathbf{A}_j^{-1} \mathbf{R}_j^T$$

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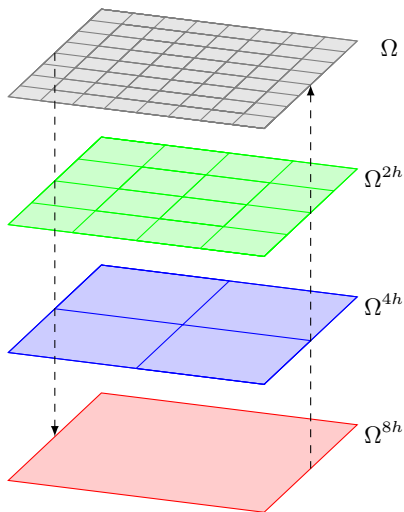
R_j are elliptic projection operators, and \mathbf{A}_j are the restriction of $a_\epsilon(\cdot, \cdot)$ to a subdomain Ω_j .

Schwarz domain decomposition

Partition the domain $\Omega = \cup_{j=1}^{N_S} \Omega_j$, pick a coarse grid (Ω^{2h} , Ω^{4h} , or Ω^{8h})

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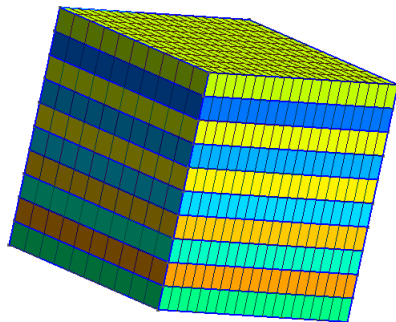
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- The **coarse** grid operator $(\mathbf{R}_0 \mathbf{A}_0^{-1} \mathbf{R}_0^T)$ is amenable to **coarse** grain parallelism
- The **fine** grid operators $(\mathbf{R}_j \mathbf{A}_j^{-1} \mathbf{R}_j^T)$ are amenable to **fine** grain parallelism

Hybrid computing strategy

- Global partition of mesh

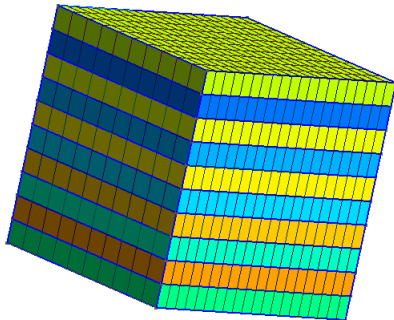
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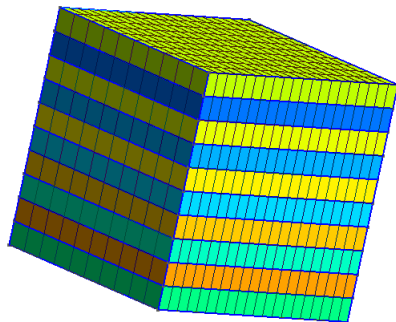
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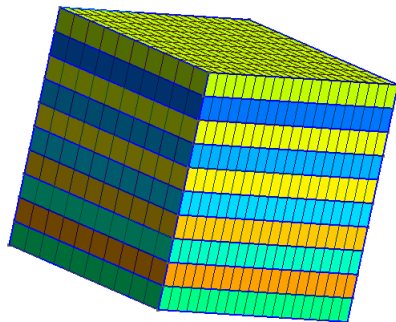
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- Each layer of hexahedra is processed by MPI
- Layers are sent to coprocessors for fine grain parallelism
- Coarse grid is handled by a small number of MPI ranks

Closer look at mapping to coprocessor

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